



FORM PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) (37 CFR 1.98(b))	ATTY. DOCKET NO.: FIS9-1999-0319-US1	SERIAL NO.:
	APPLICANT: Harry J. Beatty, III et al.	
	FILING DATE:	GROUP:

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
SJA	AA	5,129,080	7/7/92	Smith			
SJA	AB	5,353,418	10/4/94	Nikhil et al.			
SJA	AC	5,386,525	1/31/95	Noack			
SJA	AD	5,928,323	7/27/99	Gosling et al.			
SJA	AE	5,937,194	8/10/99	Sundaresan			
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB- CLASS	TRANSLATION	
							YES	NO
SJA	AL	EP 0 897 150 A2	17.02.99	EPC				
SJA	AM	EP 0 909 094 A1	14.04.99	EPC				
SJA	AN	WO 99/10811	04.03.99	PCT				
SJA	AO	EP 0 936 544 A2	18.08.99	EPC				
SJA	AP	EP 0 899 660 A1	03.03.99	EPC				

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

SJA	AQ	IBM Technical Disclosure Bulletin, Vol. 37, No. 01, January 1994, <i>Multisequencing In A Single Instruction Stream Processing Of Parallel Streams</i> , pages 133-139.
SJA	AR	IBM Technical Disclosure Bulletin, Vol. 36, No. 12, December 1993, <i>Implementing A CSR-PCS as Multiple Threads</i> , pages 57-61.
SJA	AS	Krishna Kavi, Hyong-Shik-Kim, Joseph Arul of University of Alabama; Ali R. Hurson of Pennsylvania; <i>A Decoupled Scheduled Dataflow Multithreaded Architecture</i> ; IEEE Proceedings, 8/99, pages 138-143.

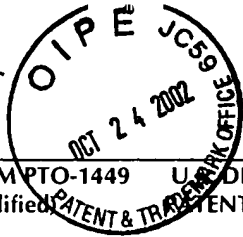
EXAMINER

SJA

DATE CONSIDERED

7/24/03

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Modified) PATENT & TRADEMARK OFFICE	ATTY. DOCKET NO.: FIS9-1999-0319-US1	SERIAL NO.: 09/597,524
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT: Harry J. Beatty, III, et al.
(Use several sheets if necessary) (37 CFR 1.98(b))	FILING DATE: June 20, 2000	GROUP: 2151

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
syn	AA	6,058,460	05/02/2000	Nakhimovsky			
syn	AB	6,349,312	02/19/2002	Fresko et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

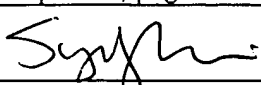
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB- CLASS	TRANSLATION YES NO
	AL						
	AM						
	AN						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

syn	AO ✓	C. B. Zilles, J. S. Emer and G. S. Sohi; "The Use Of Multithreading For Exception Handling"; 1999 IEEE, pages 219-229.
syn	AP ✓	T. Domani, G. Goldshtein, E. K. Kolodner, E. Lewis, E. Petrank, D. Sheinwald; "Thread-Local Heaps For Java", June 20-21, 2002, ISMM, pages 76-87.
syn	AQ ✓	D. Dice, A. Garthwaite, "Mostly Lock-Free Malloc", June 20-21, 2002, ISMM, pages 163-174.
syn	AR ✓	J. S. Chase, H. M. Levy, M. J. Feeley and E. D. Lazowska, "Sharing And Protection In A Single-Address-Space Operating System", ACM Transactions on computer Systems, Nov. 1994, Vol. 12, No. 4, pages 271-307.

EXAMINER 	DATE CONSIDERED 7/24/03
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